

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Error ro rs
1	BRS	L1	0	((circuit adj board or substrate) same (cooling same (loop or channel\$1) and (coinciding adj via\$1)	USPA T	2004/08/1 8 09:38			0
2	BRS	L2	0	((circuit adj board or substrate) same (cooling same (loop or channel\$1) and (coinciding adj via\$1)	USPA T	2004/08/1 8 09:39			0
3	BRS	L3	1	((circuit adj board or substrate) same (cooling same (loop or channel\$1) and (coinciding adj via\$1)	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 09:39			0
4	BRS	L4	1224				USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB		
5	BRS	L5	160	(361/\$.cccls. or 174/\$.cccls. or 29/\$.cccls.) and 4	USPA T	2004/08/1 8 09:41			0

Type	L #	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Er ro rs
6 BRS	L6	131	((circuit adj board or substrate) and (cooling same (loop or channel\$1) same (layer\$1 same via\$1)	USPA T; US-P GPUB ;	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 09:58		0
7 BRS	L7	20	6 and 5	USPA T; US-P GPUB ;	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 10:01		0
8 BRS	L8	111	6 not 7	USPA T; US-P GPUB ;	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 10:01		0